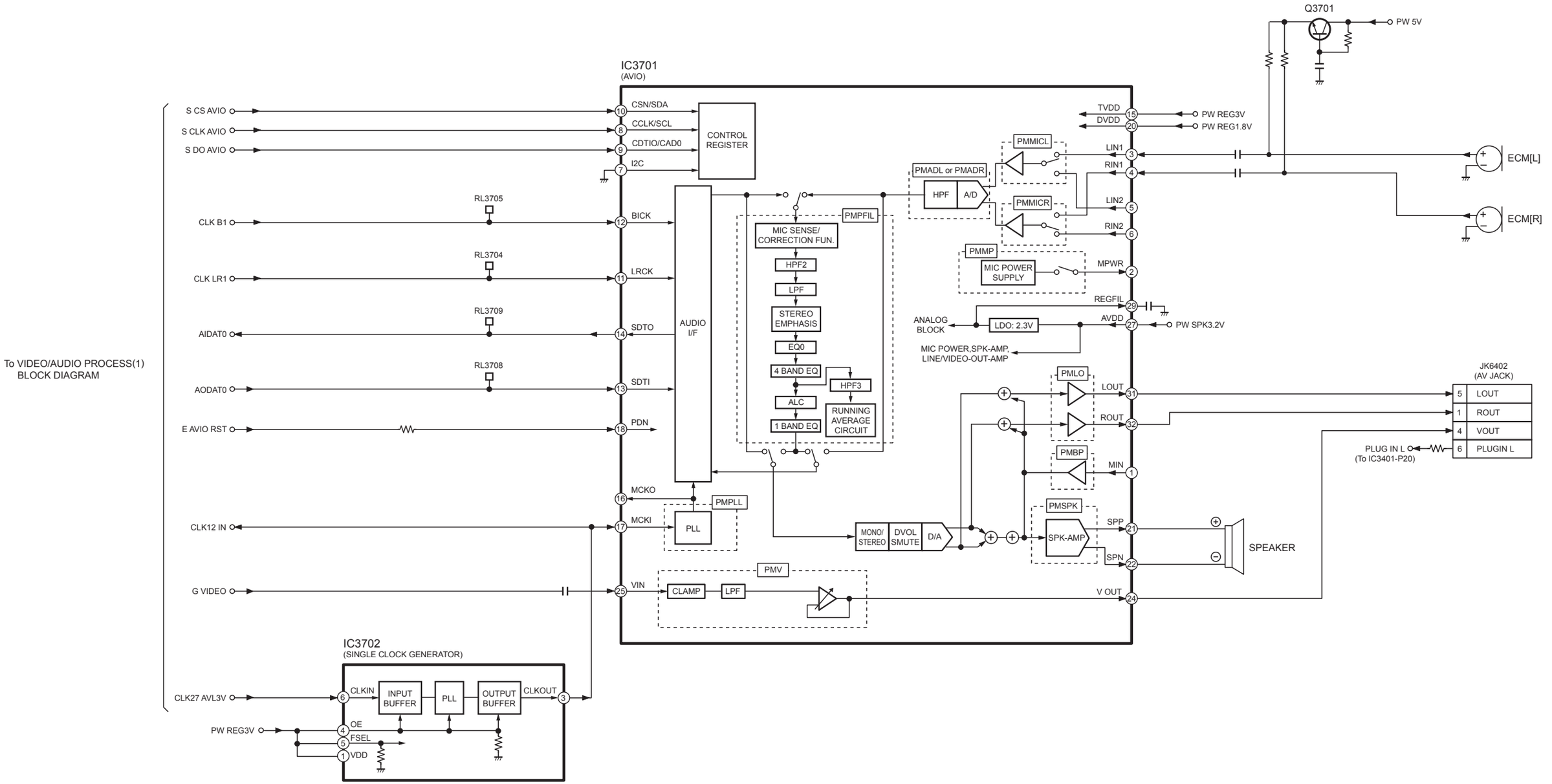
To VIDEO/AUDIO PROCESS(1) BLOCK DIAGRAM
IC3701
(AVIO)
S CS AVIO
S CLK AVIO
S DO AVIO
CSN/SDA
CCLK/SCL
CDTIO/CAD0
I2C
CONTROL REGISTER
RL3705
CLK B1
BICK
RL3704
CLK LR1
LRCK
RL3709
AIDAT0
SDTO
RL3708
AODAT0
SDTI
E AVIO RST
PDN
AUDIO I/F
MCKO
PMPLL
CLK12 IN
MCKI
PLL
G VIDEO
VIN
PMV
CLAMP
LPF
PMPFIL
MIC SENSE/ CORRECTION FUN.
HPF2
LPF
STEREO EMPHASIS
EQ0
4 BAND EQ
HPF3
ALC
1 BAND EQ
RUNNING AVERAGE CIRCUIT
PMADL or PMADR
HPF
A/D
PMMICL
PMMICR
LIN1
RIN1
LIN2
RIN2
PMMP
MIC POWER SUPPLY
MPWR
REGFIL
ANALOG BLOCK
LDO: 2.3V
AVDD
MIC POWER,SPK-AMP, LINE/VIDEO-OUT-AMP
TVDD
DVDD
PW REG3V
PW REG1.8V
PW SPK3.2V
PW 5V
Q3701
ECM[L]
ECM[R]
PMLO
LOUT
ROUT
PMBP
MIN
PMSPK
SPP
SPN
SPK-AMP
MONO/ STEREO
DVOL SMUTE
D/A
SPEAKER
V OUT
JK6402
(AV JACK)
LOUT
ROUT
VOUT
PLUGIN L
PLUG IN L
(To IC3401-P20)
IC3702
(SINGLE CLOCK GENERATOR)
CLK27 AVL3V
PW REG3V
CLKIN
INPUT BUFFER
PLL
OUTPUT BUFFER
CLKOUT
OE
FSEL
VDD